## BACHELOR OF COMPUTER SYSTEMS ENGINEERING (HONOURS)

**Program Plan**

Commencing in Semester 2

**Start Date:** 2017 to 2020

**Location:** Callaghan

This Program Plan is an enrolment guide to ensure you are on track to graduate. If at any time you wish to vary from this program plan seek advice from your Program Advisor to ensure you remain on track.

### Program Plan

<table>
<thead>
<tr>
<th>Year 1</th>
<th>Semester 1</th>
<th>ENGG1500 Introduction to Professional Engineering</th>
<th>CORE</th>
<th>ENGG1003 Introduction to Procedural Programming</th>
<th>CORE</th>
<th>MATH1120 Mathematics for Engineering, Science and Technology 1</th>
<th>CORE</th>
<th>SENG1110 Object Oriented Programming</th>
<th>CORE</th>
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<tbody>
<tr>
<td></td>
<td>Semester 2</td>
<td>SENG1120 Data Structures</td>
<td>CORE</td>
<td>ENGG2250 System and Network Security</td>
<td>CORE</td>
<td>ELEC1310 Introduction to Electrical Engineering</td>
<td>CORE</td>
<td>MATH1210 Mathematical Discovery 1</td>
<td>CORE</td>
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<td></td>
<td></td>
<td>ENGG1220 Electrical and Electronic Circuits</td>
<td>CORE</td>
<td>ELEC2430 Circuits and Signals</td>
<td>CORE</td>
<td>MATH1110 Mathematics for Engineering, Science and Technology 1</td>
<td>CORE</td>
<td>STAT2110 Engineering Statistics</td>
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<td></td>
<td></td>
<td>ELEC3500 Telecommunication Networks</td>
<td>CORE</td>
<td>ELEC3540 Analog and Digital Communications</td>
<td>CORE</td>
<td>ELEC3240 Analog Electronics</td>
<td>CORE</td>
<td>ELEC3500 Telecommunication Networks</td>
<td>CORE</td>
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<td></td>
<td></td>
<td>ELEC4720 Programmable Logic Design</td>
<td>CORE</td>
<td>ELEC4820A Final Year Engineering Project Part A</td>
<td>CORE</td>
<td>ELEC4840A Project Part A</td>
<td>CORE</td>
<td>ELEC4720 Programmable Logic Design</td>
<td>ELEC</td>
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<tr>
<td></td>
<td></td>
<td>ELEC4840B Final Year Engineering Project Part B</td>
<td>DIRECTED</td>
<td>ELEC3730 Digital and Computer Electronics 2</td>
<td>CORE</td>
<td>ELEC4840B Final Year Engineering Project Part B</td>
<td>CORE</td>
<td>ELEC4840B Final Year Engineering Project Part B</td>
<td>ELEC</td>
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**Compulsory Professional Practice:**

**Industrial Experience 12 Weeks**

If you have any questions visit [NEWCASTLE.EDU.AU/ASKUON](NEWCASTLE.EDU.AU/ASKUON)
To be eligible to graduate make sure you have completed 320 units (10 units = 1 course unless otherwise specified) which meet the following criteria:

- Core courses – 270 units
- Directed courses – 10 units
- Electives – 40 units, visit the Program Handbook for more information
- Students must not exceed 120 units at 1000 level in this program
- It is also a requirement that students complete a total of 12 weeks of industrial experience.
- The duration of this program is 4 year full-time (40 units per semester) or part-time equivalent.
- The maximum time to complete this program is 10 years.

Some courses have assumed knowledge and/or requisites, please refer to the individual Course Handbook. Please refer to the Program Handbook for specific information on program structure. If you are intending varying from this program plan please seek advice from your Program Advisor.
DIRECTED COURSES

Complete 10 units from:

- ELEC3400: Signal Processing
- COMP3330: Machine Intelligence
- PHYS3360: Advanced Electromagnetism
- ELEC4210: Electronics Design
- ELEC4740: Internet of Things